



<b>Job Title:</b>	Senior Software Engineer – FPGA Programmer
<b>Department:</b>	Engineering
<b>Reports to:</b>	Vice President Engineering
<b>Location:</b>	Irvine, California

**Job Title:**  
Senior Software Engineer – FPGA Programmer

**Job Description:**  
Develop FPGA designs of medium complexity for FPGAs using VHDL. Application is comprised of DO-254 Avionics development. Must have the ability to work with System Engineering to define requirements, implement functionality, verify integrate and maintain FPGA designs. Excellent understanding of structured design and experience working the Designated Engineering Representatives/ Designated Airworthiness Representatives (DERs/DARs) a plus. Other duties may include assignment of C++ Software design for Real Time Operating Systems.

The candidate must be energetic and comfortable working in a fast-paced agile environment. This position requires a candidate with strong interpersonal and self-motivational skills in order to meet deadlines. This candidate must be capable of working independently and with a minimum of supervision. He/she should be capable of working well with other software engineers in producing the detailed design and implementation of ITAN products.

**Skills/Background:**  
The necessary skills for this job are the following:

- Ability to "think outside the box"
- Demonstrated experience with SoC or FPGA related product development
- Experienced VHDL Logic design and verification in accordance with DO-254
- Experience with RTOS/micro-kernel, ARM compiler, ARM architecture and schematics
- Some experience programming in Java, C++
- Familiar with application development for Linux and Android platforms.
- Integration and optimization of software developed by a team for specific function.
- Excellent communication skills with peers and senior leadership
- US Citizenship Required
- Ability to Obtain Interim and Final DoD SECRET Security Clearance