



Job Title:	Senior Hardware Engineer
Department:	Engineering
Reports to:	Vice President/ Engineering
Location:	Irvine, California
Salary Range:	\$90-\$125k

Job Title:
Senior Hardware Engineer

Projects:
Sharkfin, iTV and some sustaining

Job Description:
Digital design, PCB board , and schematic capture, including FPGA's.. This individual will work closely with other hardware and embedded software engineers to develop the client IFE equipment. The ideal candidate will be well versed in embedded design and have knowledge of embedded firmware FPGA state machines. This individual must also be comfortable being a Project Engineer, assuming responsibility (cost, schedule, performance) of the entire LRU (Line Replacement Unit). Must be comfortable working in small teams, be "hands-on" and capable of generating own BoMs.

Skills/Background:
The necessary skills for this job are the following:

- Experience in highly complex hardware designs with embedded FPGA's
- Integration and test experience in complex systems with FPGA's
- Orcad schematic capture or equivalent
- PADS Gerber/ or alternate viewer files
- Design rules for layout
- Project engineering responsibility
- Knowledge of manufacturing engineering best practices
- Some components sourcing and design-to-cost principles
- Experience with avionics or similar industrial equipment
- Ability to bring together sub-assemblies as well design engineering
- Knowledge of networking and video a plus
- Knowledge of Linux and C/C++ programming environments a plus